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	Application No.	Applicant(s)	Gon
Nation of Allawahility	10/765,699	KATZ, ZACHARY B.	
Notice of Allowability	Examiner	Art Unit	
	Thao P. Le	2818	
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in to or other appropriate commur GHTS. This application is su	this application. If not included nication will be mailed in due cour	se. THIS
1. This communication is responsive to <u>01/26/04</u> .			
2. ☑ The allowed claim(s) is/are <u>1-39</u> .			
3. $igotimes$ The drawings filed on <u>26 January 2004</u> are accepted by the	e Examiner.		
 4. Acknowledgment is made of a claim for foreign priority un a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Application	No	from the
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the require	ements
 A SUBSTITUTE OATH OR DECLARATION must be submi INFORMAL PATENT APPLICATION (PTO-152) which give 	itted. Note the attached EXAI es reason(s) why the oath or	MINER'S AMENDMENT or NOTI declaration is deficient.	CE OF
6. CORRECTED DRAWINGS (as "replacement sheets") mus	t be submitted.		
(a) including changes required by the Notice of Draftspers	on's Patent Drawing Review	(PTO-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date			
(b) including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or i	in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the	.84(c)) should be written on the he header according to 37 CFF	e drawings in the front (not the bac R 1.121(d).	k) of
7. DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MATE	RIAL must be submitted. Note	the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Su Paper No./N	ormal Patent Application (PTO-15 mmary (PTO-413), Mail Date	52)
 Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date <u>2 pages</u> 	98), 7. ∐ Examiner's A	Amendment/Comment	
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material □ 19 0 6	8. ⊠ Examiner's S 9. □ Other	Statement of Reasons for Allowar	nce

DETAILED ACTION

Claims 1-39 are pending.

Information Disclosure Statement

The information disclosure statement (IDS) submitted on 01/26/04 was filed on the same mailing date of the application. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Reason for allowance

Claims 1-39 are allowed. The following is an examiner's statement of reason for allowance:

Set of claims 1-14: None of the references of record teaches or suggests the claimed limitations having a method of forming a planarized surface over a semiconductor substrate comprising a non-planar surface topography substrate, forming a spin-on-dielectric layer over the substrate, forming a second dielectric over the spin-on-dielectric layer, and polishing the second dielectric layer and the spin-ondielectric layer to form an electrically insulative surface comprising both the spin-ondielectric material and the second dielectric material.

Set of claims 15-27: None of the references of record teaches or suggests the claimed limitations having a method of forming a planarized surface over a semiconductor substrate comprising a memory array region which has a different average elevational height than the a peripheral region, forming a polysilazane layer

over both regions, forming a second material over the polysilazane layer, planarizing the second material and the polysilazane layer to form a planarized surface comprising primarily the polysilazane over one of the memory array and peripheral regions and comprising primarily the second material over the other of the memory array and peripheral regions.

Set of claims 28-39: None of the references of record teaches or suggests the claimed limitations having a method of forming a planarized surface over a semiconductor substrate comprising a memory array region which has a different average elevational height than the a peripheral region, forming a spin-on-dielectric layer over both regions, forming a second material over the spin-on-dielectric layer, planarizing the second material and the spin-on-dielectric layer to form a planarized surface comprising primarily the spin-on-dielectric over one of the memory array and peripheral regions and comprising primarily the second material over the other of the memory array and peripheral regions.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thao P. Le whose telephone number is 571-272-1785. The examiner can normally be reached on M-T (7-6).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on 571-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Thao P. Le Art Unit 2818 January 19, 2006.